

## CLAIMS:

1. A system comprising a carrier and a matrix display with a substrate and display pixels,

the display comprises display conductors for supplying display signals to the display pixels,

5 the carrier comprising carrier conductors for carrying input signals, wherein the system further comprises means for at least one of a capacitive, inductive and optical transport of the input signals from the carrier conductors of the carrier to the display conductors of the display via the substrate.

10 2. A system as claimed in claim 1, wherein the means for capacitive, inductive or optical transport comprise the display conductors and the carrier conductors being positioned with respect to each other to obtain a capacitor or mutually coupled inductors between associated ones of the display conductors and the carrier conductors to capacitively, or inductively transfer the input signals on the carrier conductors to the display conductors.

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3. A system as claimed in claim 1, wherein the carrier conductors and the display conductors are positioned at opposite sides of the substrate, the substrate forming a dielectric medium of the capacitor or a magnetic medium of the transformer.

20 4. A system as claimed in claim 3, wherein a thickness of the substrate is less than 250 micron.

5. A system as claimed in claim 1, wherein the substrate comprises glass or plastic.

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6. A system as claimed in claim 1, wherein the substrate is flexible.

7. A system as claimed in claim 3, wherein associated ones of the carrier conductors and the display conductors have pads which are oppositely positioned to form the capacitor.

8. A system as claimed in claim 1, wherein the display comprises a further substrate, the first mentioned substrate and the further substrate sandwiching the display pixels, a strip of glue being provided at edges of the first mentioned substrate and the further substrate for mechanically connecting the first mentioned substrate and the further mentioned substrate, the means for capacitive, inductive or optical transport being arranged to substantially coincident with the strip of glue.

9. A system as claimed in claim 1, wherein the display comprises a buffer with a buffer input coupled to one of the display conductors to receive an associated one of the capacitively, inductively, or optically coupled input signals and a buffer output to supply the associated one of the display signals to the associated one of the display pixels, the buffer comprises a transistor having a control electrode for receiving the capacitively, inductively, or optically coupled input signals.

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10. A system as claimed in claim 9, wherein the buffer comprises a first transistor, a second transistor, and a third transistor,

the first transistor has a control electrode coupled to the buffer input to receive the input signal, a first main electrode coupled to a first voltage level, and a second main electrode coupled to the buffer output to supply the display signal,

the second transistor has a control electrode coupled to receive a clock signal, a first main electrode coupled to the buffer output, and a second main electrode coupled to a second voltage level being different than the first voltage level, and

the third transistor has a control electrode coupled to receive the clock signal, a first main electrode coupled to the buffer input, and a second main electrode coupled to the second voltage level.

11. A system as claimed in claim 1, wherein the display pixels are arranged in a matrix of columns and rows, the system further comprising a shift register for successively selecting a line of the pixels, or to supply a line of data to selected pixels, both in either the direction of the columns or the rows.

12. A system as claimed in claim 1, wherein the input signals comprise a clock signal and/or a reference level.

13. A system as claimed in claim 1, wherein the system comprises means for capacitively or inductively coupling power to the display.

5 14. A system as claimed in claim 13, wherein the means for capacitively or inductively coupling comprises two cooperating pads arranged at opposite sides of the substrate, and having an area to obtain a capacitor which is substantially larger than a capacitor required to transfer the input signals from the carrier conductors to the display conductors.

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15. A system as claimed in claim 13, wherein the means for capacitively or inductively coupling comprises two cooperating inductors arranged as tracks at opposite sides of the substrate.

15 16. A system as claimed in claim 15, wherein the two cooperating inductors each form a closed loop.

17. A system as claimed in claim 1, wherein means for capacitive, inductive or optical transport comprises a light generating element on the carrier and a corresponding light sensitive element on the display, the substrate being transparent to a light generated by the light generating element to obtain an optical transport from the light generating element to the light sensitive element.

18. A display apparatus comprising a system as claimed in claim 1.

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19. A display comprising one of the two cooperating pads as claimed in claim 14, the display further comprises a rectifier for rectifying an AC-voltage when present at the one of the two cooperating pads.

30 20. A display comprising one of the two cooperating inductors as claimed in claim 15, the display further comprises a rectifier for rectifying an AC-voltage when present at the one of the two cooperating inductors.